

## APPENDIX

### *In The Claims*

Please cancel claims 31, 37, 47, 69 and 71-80 without any disclaimer or a prejudice to and amend claims 29, 32, 36, 64 and 71, as follows.

29. (Three times Amended) A method for manufacturing a thin film transistor array panel for a liquid crystal display, comprising the steps of:

forming a gate wire including a gate line and a gate electrode connected to the gate line on an insulating substrate;

forming a gate insulating layer covering the gate wire;

forming a semiconductor pattern on the gate insulating layer;

forming a data wire including a source electrode, a drain electrode and a data line connected to the source electrode on the semiconductor pattern, wherein the gate insulating layer, the semiconductor pattern and the data wire are patterned in a single photolithography step;

forming a passivation layer covering a color filter before forming red, green and blue color filters;

forming color filters made of photosensitive material, the color filters covering data wire and having a first contact hole; and

forming a pixel electrode connected to the drain electrode through the first contact hole of the color filters.

32. (Amended) The method of claim 29[31], wherein the passivation layer is made of

photosensitive transparent organic material having a good planarization property.

46. (Twice Amended) A thin film transistor array panel for a liquid crystal display, comprising:

a gate wire including a gate line and a gate electrode connected to the gate line, and formed on an insulating substrate;

a gate insulating layer covering the gate electrode;

a semiconductor pattern formed on the gate insulating layer;

a data wire including a source electrode and a drain electrode, and a data line connected to the source electrode;

a passivation layer covering the data wire and having a first contact hole exposing the drain electrode; [and]

red, green, and blue color filters formed under the passivation layer; and

a pixel electrode connected to the drain electrode through the first contact hole,

wherein the gate wire or the data wire are made of photodefinable conductive material.

48. (Amended) The thin film transistor array panel of claim 46[47], wherein the passivation layer and the color filters are made of photosensitive material.

64. (Twice mended) A thin film transistor array panel for a liquid crystal display, comprising:

a plurality of gate lines formed on an insulating substrate;

a gate insulating layer covering the gate lines;

a plurality of data lines intersecting the gate lines;

an amorphous silicon layer formed under the entire data lines;

an ohmic contact layer interposed between the data lines and the amorphous silicon layer;  
an array of thin film transistors, each transistor including a gate electrode connected to  
the gate line, a source electrode connected to the data line, and a drain electrode;  
a plurality of color filters formed over the gate insulating layer, the color filters  
overlapping the data lines at least in part; and  
a plurality of pixel electrodes formed on the color filters, each pixel electrode electrically  
connected to the drain electrode.

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overlapping the data lines at least in part; and

a plurality of pixel electrodes formed on the color filters, each pixel electrode electrically connected to the drain electrode.

### **REMARKS**

In response to the Advisory Action dated September 24, 2002, Applicants file the above mentioned amendments and following remarks. Claims 1-12, 29-36, 38-49 and 64-65 and 67-70 are active in this application, of which claims 1, 29, 34, 38, 46 and 64 are independent. By this amendment, claims 31, 37, 47, 69 and 71-80 have been cancelled and claims 29, 32, 46, 48 and 64 have been amended.

The final Office Action indicated that claims 1-12, 34 and 38-45 are allowed, and claims 31-33, 47-49 and 69-70 are allowable but objected to for being dependent from the rejected base claims.

By this amendment, the subject matter of claim 31 is incorporated in claim 29. Also the subject matter of claim 47 is incorporated in claim 46 and the subject matter of claim 69 is now incorporated in claim 64. Therefore, newly amended claims 29, 46 and 64 are now also allowable over the references of record. Likewise, claims 30-33 and 35-36 that are dependent from claim 29 are now also allowable. Also, claims 48-49 that are dependent from claim 46 are now allowable. In the same context, claims 65, 67-68 and 70 that are dependent from claim 64 are also allowable.

All other rejections and objections are now moot, because claims other than allowed or made allowable by this amendment are cancelled in this amendment.

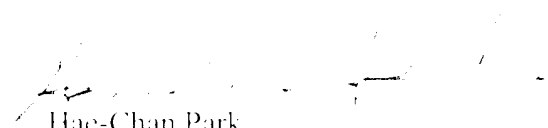
Accordingly, the Examiner is respectfully requested to withdraw all the outstanding rejections and objections over claims 1-12, 29-30, 32-36, 38-46, 48-49, 64-65, 67-68 and 70 be withdrawn, and pass those claims to allowance.

### **CONCLUSION**

All of the stated grounds of objection and rejection have been properly traversed, accommodated, or rendered moot. Applicants therefore respectfully request that the Examiner reconsider all presently outstanding objections and rejections and that they be withdrawn. Applicants believe that a full and complete response has been made to the outstanding Office Action and, as such, claims 1-12, 29-30, 32-36, 38-46, 48-49, 64-65, 67-68 and 70 are in condition for allowance. If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

Prompt and favorable consideration of this Amendment is respectfully requested.

Respectfully submitted,

  
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